



## Internship Opportunity: Staff Design, Validation, and Test Engineer

indie's internship program is designed for driven individuals who are looking for hands-on, real-world job experience with guidance and mentorship. Our program focuses on expanding skill sets, building upon education, and providing professional support within the autotech industry. Interns will engage with actual customer projects, both in the lab and with indie's global teams, with the opportunity to provide meaningful contributions to new software development, product designs, and more.

### internship Overview:

indie is recruiting outstanding talents to build innovative system solutions with integrated micro controllers and custom ASICs across various segments including automotive infotainment, home automation, medical and RF connectivity. In particular, we are currently looking for a Design Validation and Test (DVT) application engineering intern for wireless charging. Our ideal candidate should have deep and solid understanding on basic EE concepts and can apply them in real life lab work. The successful candidate will be responsible for defining and executing the validation plans for new semiconductor products at IP (in situ characterization) and system levels. Close collaboration with design team is encouraged as our IC designers do spend time in the lab!

### Job Summary:

- Validate indie's ICs functionality in the lab against design and application requirements. Tasks include test plan and procedure generation, initial chip bring up, full IP and chip level validation and characterization over process and temperature and debug analysis while product is in development stage.
- Define and develop the test setups with reasonable tradeoffs between accuracy, measurement time, and cost.
- Develop automation software for the tests using LabView or Python preferably.
- Design hardware fixtures, e.g. a PCB, as a part of the test setup if no off-the-shelf solution is available.
- Collect, consolidate and analyze test data for review with the design and quality teams.
- Participate in board and chip design reviews to make sure all the parameters can be tested.
- Help implement design features for testing and automation purposes.

### Requirements:

- BS degree in Electrical, Computer Engineering and/or related experience.
- Have a solid foundation on basic circuit and demonstrated good understanding on basic analog circuits on areas like transistors, Op Amp, ADC, DAC, oscillator and clock.
- Proficient with various test bench equipment such as DMM, power supply, oscilloscope, spectrum analyzer, signal generator and VNA.
- Hands on capability to design and implement EDVT characterization tests with solid understanding of hardware design and manufacturing test parameters. Familiar with concepts such as spec compliance matrix.
- Enjoy debugging as well as inventing more efficient ways to test indie's ICs.



- Comfortable with MCU based systems and FW development tool chain.
- Hands on capability to develop and implement test scripts using C, C++, LabView and Python programming.
- Be systematic and very well organized, excellent communication skills as we do have offices in UK, Austin and California with FAE presence in Asia.

**Internship Location:** Aliso Viejo, CA