



Internship Opportunity: Digital IC Design

indie's internship program is designed for driven individuals who are looking for hands-on, real-world job experience with guidance and mentorship. Our program focuses on expanding skill sets, building upon education, and providing professional support within the Autotech industry. Interns will engage with actual customer projects, both in the lab and with indie's global teams, with the opportunity to provide meaningful contributions to new software development, product designs, and more.

internship Overview:

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The design team at indie Semiconductor develops custom automotive centric ICs using ARM Cortex-M based microcontrollers and integrates Analog and mixed-signal, RF and Power Management IPs bringing a complete solution to our international customer base. The engineering intern in this position will work with a team of highly skilled engineers who are creating brand-new designs in the field of Wireless and USB power delivery, sensor systems, connectivity, and networking with associated MCU technologies. In this position, the ASIC Engineering Intern may participate in design, development, verification, and bench testing of a wide range of indie ASIC's. Additionally, the ASIC Engineering intern will support team members in validating ASIC functionality with system-level testing.

Job Summary:

- Write and document RTL to implement digital IP based on a design specification. Develop block level test benches with test cases to verify functionality and debug failures
- Support DVT lab activities such as circuit characterization
- Support automation through scripting (eg. Python scripting in DVT)
- Support ASIC system level bring-up
- Learn and Support DFT (Design For Test) design through the simulation of ATPG vectors

Requirements:

- Current student with degree in electrical engineering (EE) or computer engineering (CE)
- Familiarity with RTL (Verilog) and scripting (Python, Ruby, or Perl)
- Familiarity with debugging and data analysis
- Familiarity with revision control systems (i.e. SVN, Git)
- Knowledge of lab equipment tools such as oscilloscope, digital multimeter, and/or digital logic analyzer
- Demonstrate knowledge of designing and verifying digital blocks using Verilog code.
- Energetic contributor with the ability to learn fast and communicate effectively

Internship Location: Aliso Viejo, CA