



## Internship Opportunity: Analog/Mixed-Signal IC Design

indie's internship program is designed for driven individuals who are looking for hands-on, real-world job experience with guidance and mentorship. Our program focuses on expanding skill sets, building upon education, and providing professional support within the Autotech industry. Interns will engage with actual customer projects, both in the lab and with indie's global teams, with the opportunity to provide meaningful contributions to new software development, product designs, and more.

### internship Overview:

indie's internship program is designed for driven individuals who are looking for hands-on, real-world job experience with guidance and mentorship. Our program focuses on expanding skill sets, building on your existing education and providing professional support within the Autotech industry and various fields.

The analog/mixed-signal integrated circuit (IC) design internship provides exposure to multiple areas of the IC development flow (including but not limited to circuit design, simulation, bench evaluation and debug).

### Job Summary:

- Design basic analog/mixed-signal circuits including op-amps, oscillators, comparators, etc. according to real-world specifications and robustness guidelines
- Develop Cadence simulation test benches to verify analog/mixed-signal circuits, from the smallest building block to the entire IC or SoC
- Develop behavioral models of analog/mixed-signal circuits for top-level IC simulation speedup
- Test, characterize, and debug ICs in the lab as necessary
- Analyze test data and correlate to simulation expectations

### Requirements:

- Candidate must be enrolled in an accredited Bachelor's, Master's, or Ph.D. degree program in Electrical Engineering for a minimum of 3 years if Bachelor's, or a minimum of 1 year if Master's/PhD
- Solid grasp of analog circuit design fundamentals and theory
- Excellent verbal and written communication skills, with good attention to detail
- Familiarity with Cadence design tools
- Understanding of programming languages (Verilog, SystemVerilog, Python) is a plus

**Internship Location:** Burlington, MA